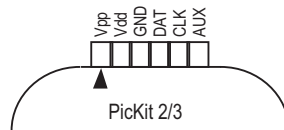


PIC18F4550

MCLR/VPP/RE3	o1	40	o	RB7/KBI3/PGD
RA0/AN0	o2	39	o	RB6/KBI2/PGC
RA1/AN1	o3	38	o	RB5/KBI1/PGM
RA2/AN2/VREF-/CVREF	o4	37	o	RB4/AN11/KBI0/CSSPP
RA3/AN3/VREF+	o5	36	o	RB3/AN9/CCP2 (1) /VPO
RA4/T0CKI/C1OUT/RCV	o6	35	o	RB2/AN8/INT2/VMO
RA5/AN4/SS/HLVDIN/C2OUT	o7	34	o	RB1/AN10/INT1/SCK/SCL
RE0/AN5/CK1SPP	o8	33	o	RB0/AN12/INT0/FLT0/SDI/SDA
RE1/AN6/CK2SPP	o9	32	o	VDD
RE2/AN7/OESPP	o10	31	o	VSS
VDD	o11	30	o	RD7/SPP7/P1D
VSS	o12	29	o	RD6/SPP6/P1C
OSC1/CLKI	o13	28	o	RD5/SPP5/P1B
OSC2/CLKO/RA6	o14	27	o	RD4/SPP4
RC0/T1OSO/T13CKI	o15	26	o	RC7/RX/DT/SDO
RC1/T1OSI/CCP2 (1) /UOE	o16	25	o	RC6/TX/CK
RC2/CCP1/P1A	o17	24	o	RC5/D+/VP
VUSB	o18	23	o	RC4/D-/VM
RD0/SPP0	o19	22	o	RD3/SPP3
RD1/SPP1	o20	21	o	RD2/SPP2

BC547/57



GET: RICK

D.D: 7 januari 2010